

L Number	Hits	Search Text	DB	Time stamp
16	1052	processor\$1 same order\$1 same (invert\$4 or inversion)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 09:59
17	1	5889941.pn. and available	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 09:55
18	294	processor\$1 same order\$1 same (invert\$4 or inversion) same (bits or word\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:04
19	197	processor\$1 same order\$1 same (invert\$4 or inversion) same (bits or word\$1) same signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:05
20	348	(processor\$1 or microprocessor\$1) same order\$1 same (invert\$4 or inversion) same (bits or word\$1) same signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:10
22	220	order\$1 near20 (invert\$4 or inversion) near20 (bits or word\$1) near20 circuit\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:09
21	162	(processor\$1 or microprocessor\$1) same order\$1 same (invert\$4 or inversion) same (bits or word\$1) same signal\$1 same circuit\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:09
23	32	(processor\$1 or microprocessor\$1) same (order\$1 near10 (invert\$4 or inversion) near10 (bits or word\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:16
24	8	((processor\$1 or microprocessor\$1) same (order\$1 near10 (invert\$4 or inversion) near10 (bits or word\$1))) and card\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:13
25	32	SIM and (order\$1 near10 (invert\$4 or inversion) near10 (bits or word\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:17
26	2	(SIM near5 card\$1) and (order\$1 near10 (invert\$4 or inversion) near10 (bits or word\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:18
27	108	(order\$1 near10 (invert\$4 or inversion) near10 (bits or word\$1) near15 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:55
28	0	(register or POS) same (printer near10 sides near10 back)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:22
30	101	(register or POS) same (print\$4 near20 front near20 back)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/18 10:23

29	10	(register or POS) same (printer near10 side\$1 near10 back)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/18 10:23
31	24	(((register or POS) same (printer near10 side\$1 near10 back)) or ((register or POS) same (print\$4 near20 front near20 back))) and (point-of-sale or POS or retail)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/18 10:27
32	345	(first near20 second near20 printer\$1) and (point-of-sale or POS or retail)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/18 10:34
33	25	(front same back same first same second same printer\$1) and (point-of-sale or POS or retail)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/18 10:30
34	167	(first near4 printer) and (second near4 printer\$1) and (point-of-sale or POS or retail)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/18 10:36
35	7	(order\$1 near10 (invert\$4 or inversion) near10 (bits or word\$1) near15 circuit) same (CPU or microprocessor or processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2002/10/18 11:00